

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kiyosshi Kato Art Unit : Unknown
Serial No. : New Application Examiner : Unknown
Filed : April 27, 2006
Title : SEMICONDUCTOR INTEGRATED CIRCUIT AND DESIGN METHOD THEREOF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATEMENT REGARDING POWER OF ATTORNEY

Under 37 C.F.R. § 1.32(c)(3), please recognize the following registered patent practitioners associated with Customer No. 26171 as representatives for the above-referenced application.

John F. Hayden, Reg. No. 37,640
W. Karl Reuner, Reg. No. 41,265
Gregory A. Walters, Reg. No. 41,366
Thomas A. Kozylowicz, Reg. No. 50,620
Barbara A. Beuch, Reg. No. 54,777

Matthew T. Shanley, Reg. No. 47,074
Robert G. Devoto, Reg. No. 55,108
Kevin E. Greene, Reg. No. 46,031
Diana DiBerardino, Reg. No. 45,653
Andrew T. Fox, Reg. No. 57,333

A signed Power of Attorney for the above-identified application is attached.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: April 27, 2006

John F. Hayden
Reg. No. 37,640

Customer No. 26171
Fish & Richardson P.C.
1425 K Street, N.W., 11th Floor
Washington, DC 20005-3500
Telephone: (202) 783-5070
Facsimile: (202) 783-2331